#### Script generated by TTT

Title: Petter: Programmiersprachen (09.11.2016)

Date: Wed Nov 09 14:16:05 CET 2016

Duration: 88:44 min

Pages: 40

## **Condition Variables**

√ Monitors simplify the construction of thread-safe resources.

Still: Efficiency problem when using resource to synchronize:

- if a thread t waits for a data structure to be filled:
  - ▶ t will call e.g. pop() and obtain -1
  - ▶ t then has to call again, until an element is available

 $\triangle$  t is busy waiting and produces contention on the lock

# Implementation of a Basic Monitor



A monitor contains a mutex count and the id of the thread tid occupying it:

```
typedef struct monitor mon_t;
struct monitor { int tid; int count; };
void monitor_init(mon_t* m) { memset(m, 0, sizeof(mon_t)); }
```

- Define monitor\_enter and monitor\_leave:

   ensure mutual exclusion of accesses to mon\_t
- track how many times we called a monitored procedure recursively

```
void monitor_enter(mon_t *m) {
                                    void monitor_leave(mon_t *m) {
                                      atomic {
 bool mine = false;
 while (!mine) {
                                        m->count--;
   atomic {
                                        if (m->count==0) {
     mine = thread_id()==m->tid;
                                          // wake up threads
     if (mine) m->count++: else
                                          m->tid=0:
        if (m->tid==0) {
         mine = true: m->count=1:
         m->tid = thread_id();
   };
   if (!mine) de_schedule(&m->tid);}}
```

# Condition Variables

Atomic Executions, Locks and Monitors

חחוווו

✓ Monitors simplify the construction of thread-safe resources.

Still: Efficiency problem when using resource to synchronize:

- if a thread t waits for a data structure to be filled:
  - t will call e.g. pop() and obtain -1
  - ▶ t then has to call again, until an element is available

 $\triangle$  t is busy waiting and produces contention on the lock

Idea: create a condition variable on which to block while waiting:

```
struct monitor { int tid; int count; int cond; int cond2;... };
```

#### **Condition Variables**

√ Monitors simplify the construction of thread-safe resources.

Still: Efficiency problem when using resource to synchronize:

- if a thread t waits for a data structure to be filled:
  - ▶ t will call e.g. pop() and obtain -1
  - ▶ t then has to call again, until an element is available

t is busy waiting and produces contention on the lock

Idea: create a condition variable on which to block while waiting:

struct monitor { int tid; int count; int cond; int cond2;... };

#### Define these two functions:

- wait for the condition to become true
  - called while being inside the monitor
  - temporarily releases the monitor and blocks
  - when signalled, re-acquires the monitor and returns
- 2 signal waiting threads that they may be able to proceed
  - one/all waiting threads that called wait will be woken up, two possibilities:

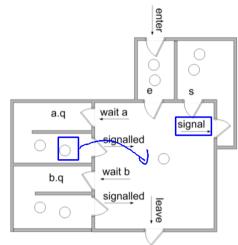
signal-and-urgent-wait: the signalling thread suspends and continues once the signalled thread has released the monitor

signal-and-continue the signalling thread continues, any signalled thread enters when the monitor becomes available

Atomic Executions, Locks and Monitors

# **Signal-And-Urgent-Wait Semantics**

Requires one queues for each condition c and a suspended queue s:

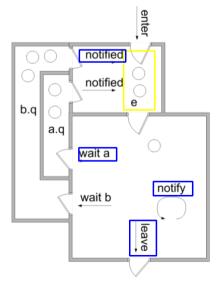


SOURCE: http://en.wikipedia.org/wiki/Monitor\_(synchronization)

- a thread who tries to enter a monitor is added to gueue e if the monitor is occupied
- a call to wait on condition a adds thread to the queue a.q
- a call to signal for a adds thread to queue s (suspended)
- one thread form the a queue is woken up
- signal on a is a no-op if a.q is empty
- if a thread leaves, it wakes up one thread waiting on s
- if s is empty, it wakes up one thread from e

# **Signal-And-Continue Semantics**

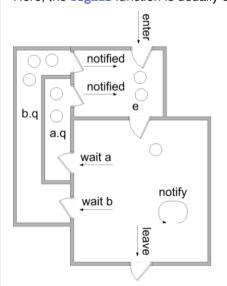
Here, the signal function is usually called notify.



- a call to wait on condition a adds thread to the queue a.q
- a call to notify for a adds one thread from a.q to e (unless a.q is empty)
- if a thread leaves, it wakes up one thread waiting on e

# **Signal-And-Continue Semantics**

Here, the signal function is usually called notify.



- a call to wait on condition a adds thread to the queue a.q
- a call to notify for a adds one thread from a.q to e (unless a.q is empty)
- if a thread leaves, it wakes up one thread waiting on e
- → signalled threads compete for the monitor
  - assuming FIFO ordering on e, threads who tried to enter between wait and notify will run first
  - need additional queue s if waiting threads should have priority

SOURCE: http://en.wikipedia.org/wiki/Monitor\_(synchronization)

SOURCE: http://en.wikipedia.org/wiki/Monitor\_(synchronization)

Atomic Executions, Locks and Monitors



# **Implementing Condition Variables**



We implement the simpler *signal-and-continue* semantics:

• a notified thread is simply woken up and competes for the monitor

```
void cond_wait(mon_t *m) {
 assert(m->tid==thread_id());
 int old count = m->count:
 m->tid = 0;
 wait(m->cond);
 bool next_to_enter;
 do {
                                       void cond_notify(mon_t *m) {
   atomic {
                                         // wake up other threads
                                        signal(m->cond);
     next_to_enter = m->tid==0;
     if (next_to_enter) {
       m->tid = thread_id();
      m->count = old_count;
   if (!next_to_enter) de_schedule(&m->tid);
 } while (!next_to_enter);}
```

Atomic Executions, Locks and Monitors

Locked Atomic Executions

## A Note on Notify



With signal-and-continue semantics, two notify functions exist:

- notify: wakes up exactly one thread waiting on condition variable
- ontifyAll: wakes up all threads waiting on a condition variable

Atomic Executions, Locks and Monitors

ocked Atomic Execution

20/39

# **A Note on Notify**



With signal-and-continue semantics, two notify functions exist:

- notify: wakes up exactly one thread waiting on condition variable
- notifyAll: wakes up all threads waiting on a condition variable

△ an implementation often becomes easier if notify means notify some

--- programmer should assume that thread is not the only one woken up

# A Note on Notify



With *signal-and-continue* semantics, two notify functions exist:

- o notify: wakes up exactly one thread waiting on condition variable
- notifyAll: wakes up all threads waiting on a condition variable

an implementation often becomes easier if notify means notify some

 $\leadsto$  programmer should assume that thread is not the only one woken up

What about the priority of notified threads?

- a notified thread is likely to block immediately on &m->tid
- $\bullet \hspace{0.1cm} \rightsquigarrow \hspace{0.1cm} \text{notified threads compete for the monitor with other threads}$
- if OS implements FIFO order: notified threads will run *after* threads that tried to enter since wait was called
- giving priority to waiting threads requires more complex implementation (queue data structure for signaled threads)

Atomic Executions, Locks and Monitors

Locked Atomic Execution

00 / 00

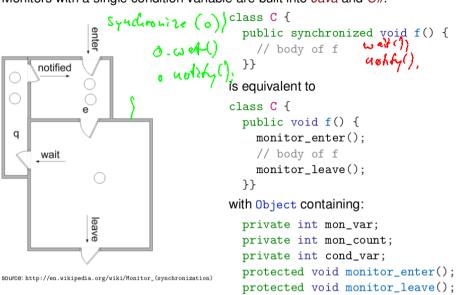
Atomic Executions, Locks and Monitors

Locked Atomic Execution

# Monitors with a Single Condition Variable



Monitors with a single condition variable are built into Java and C#:



## **Deadlocks**

#### **Deadlocks with Monitors**



#### **Definition (Deadlock)**

A deadlock is a situation in which two processes are waiting for the respective other to finish, and thus neither ever does.

(The definition generalizes to a set of actions with a cyclic dependency.)

## **Deadlocks with Monitors**



#### **Definition (Deadlock)**

A deadlock is a situation in which two processes are waiting for the respective other to finish, and thus neither ever does.

(The definition generalizes to a set of actions with a cyclic dependency.)

Consider this Java class:

class Foo { public Foo other = null; public synchronized void bar() { • a.bar() acquires the monitor of a ... if (\*) other.bar(); ...

```
and two instances:
 Foo a = new Foo():
 Foo b = new Foo();
 a.other = b b.other = a;
  // in parallel:
 a.bar() || b.bar();
```

Sequence leading to a deadlock:

- threads A and B execute a.bar() and b.bar()
- b.bar() acquires the monitor of b
- A happens to execute other.bar()
- A blocks on the monitor of b
- B happens to execute other.bar()

Atomic Executions, Locks and Monitors

#### **Deadlocks with Monitors**



#### **Definition (Deadlock)**

A deadlock is a situation in which two processes are waiting for the respective other to finish, and thus neither ever does.

(The definition generalizes to a set of actions with a cyclic dependency.)

Consider this Java class:

```
class Foo {
  public Foo other = null;
 public synchronized void bar() { • a.bar() acquires the monitor of a
    ... if (*) other.bar(); ...
and two instances:
  Foo a = new Foo();
  Foo b = new Foo():
```

a.other = b; b.other = a;

Sequence leading to a deadlock:

- threads A and B execute a.bar() and b.bar()
- b.bar() acquires the monitor of b
- A happens to execute other.bar()
- A blocks on the monitor of b
- B happens to execute other.bar()

How can this situation be avoided?

Atomic Executions, Locks and Monitors

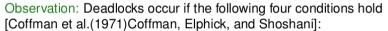
// in parallel: a.bar() || b.bar();

#### **Treatment of Deadlocks**

Observation: Deadlocks occur if the following four conditions hold [Coffman et al.(1971)Coffman, Elphick, and Shoshani]:

- mutual exclusion: processes require exclusive access
- wait for: a process holds resources while waiting for more
- on preemption: resources cannot be taken away form processes
- (a) circular wait: waiting processes form a cycle

#### **Treatment of Deadlocks**

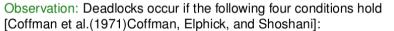


- mutual exclusion: processes require exclusive access
- wait for: a process holds resources while waiting for more
- no preemption: resources cannot be taken away form processes
- o circular wait: waiting processes form a cycle

The occurrence of deadlocks can be:

- ignored: for the lack of better approaches, can be reasonable if deadlocks are rare
- detection: check within OS for a cycle, requires ability to preempt
- prevention: design programs to be deadlock-free
- avoidance: use additional information about a program that allows the OS to schedule threads so that they do not deadlock

## **Treatment of Deadlocks**



- mutual exclusion: processes require exclusive access
- wait for: a process holds resources while waiting for more
- no preemption: resources cannot be taken away form processes
- circular wait: waiting processes form a cycle

The occurrence of deadlocks can be:

- ignored: for the lack of better approaches, can be reasonable if deadlocks
- detection: check within OS for a cycle, requires ability to preempt
- prevention: design programs to be deadlock-free
- avoidance: use additional information about a program that allows the OS to schedule threads so that they do not deadlock

→ prevention is the only safe approach on standard operating systems

- can be achieved using lock-free algorithms
- but what about algorithms that require locking?

# **Deadlock Prevention through Partial Order**



Observation: A cycle cannot occur if locks can be partially ordered.

#### **Definition (lock sets)**

Let L denote the set of locks. We call  $\lambda(p) \subseteq L$  the lock set at p, that is, the set of locks that may be in the "acquired" state at program point p.

### **Treatment of Deadlocks**



Observation: Deadlocks occur if the following four conditions hold [Coffman et al.(1971)Coffman, Elphick, and Shoshani]:

- mutual exclusion: processes require exclusive access
- wait for: a process holds resources while waiting for more
- on preemption: resources cannot be taken away form processes
- o circular wait: waiting processes form a cycle

The occurrence of deadlocks can be:

- ignored: for the lack of better approaches, can be reasonable if deadlocks are rare
- detection: check within OS for a cycle, requires ability to preempt
- prevention: design programs to be deadlock-free
- avoidance: use additional information about a program that allows the OS to schedule threads so that they do not deadlock

→ prevention is the only safe approach on standard operating systems

- can be achieved using *lock-free* algorithms
- but what about algorithms that require locking?

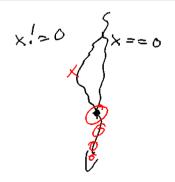
## **Deadlock Prevention through Partial Order**



Observation: A cycle cannot occur if locks can be partially ordered.

#### **Definition (lock sets)**

Let L denote the set of locks. We call  $\lambda(p) \subseteq L$  the lock set at p, that is, the set of locks that may be in the "acquired" state at program point p.



# **Deadlock Prevention through Partial Order**



Observation: A cycle cannot occur if locks can be partially ordered.

#### **Definition (lock sets)**

Let L denote the set of locks. We call  $\lambda(p) \subseteq L$  the lock set at p, that is, the set of locks that may be in the "acquired" state at program point p.

We require the transitive closure  $\sigma^+$  of a relation  $\sigma$ :

## **Definition (transitive closure)**

Let  $\sigma \subseteq X \times X$  be a relation. Its transitive closure is  $\sigma^{\dagger} = \bigcup_{i \in \mathbb{N}} \sigma^i$  where

# **Deadlock Prevention through Partial Order**



Observation: A cycle cannot occur if locks can be partially ordered.

#### **Definition (lock sets)**

Let L denote the set of locks. We call  $\lambda(p) \subseteq L$  the lock set at p, that is, the set of locks that may be in the "acquired" state at program point p.

We require the transitive closure  $\sigma^+$  of a relation  $\sigma$ :

#### **Definition (transitive closure)**

Let  $\sigma \subseteq X \times X$  be a relation. Its transitive closure is  $\sigma^+ = \bigcup_{i \in \mathbb{N}} \sigma^i$  where

$$\sigma^{0} = \sigma$$

$$\sigma^{i+1} = \{\langle x_{1}, x_{3} \rangle \mid \exists x_{2} \in X . \langle x_{1}, x_{2} \rangle \in \sigma^{i} \land \langle x_{2}, x_{3} \rangle \in \sigma^{i} \}$$

Each time a lock is acquired, we track the lock set at p:

#### **Definition (lock order)**

Define  $\lhd \subseteq L \times L$  such that  $l \triangleleft l'$  iff  $l \in \lambda(p)$  and the statement at p is of the form wait(1') or monitor\_enter(1') Define the strict lock order  $\prec = \lhd^+$ .

Atomic Executions, Locks and Monitors

Deadlocks

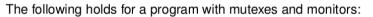
Deadlock Prevention

Deadlock

Deadlock Prevention

32 / 3

#### **Freedom of Deadlock**



#### Theorem (freedom of deadlock)

If there exists no  $a \in L$  with  $a \prec a$  then the program is free of deadlocks.

Suppose a program blocks on semaphores (mutexes)  $L_S$  and on monitors  $L_M$  such that  $L = L_S \cup L_M$ .

#### Theorem (freedom of deadlock for monitors)

If  $\forall a \in L_S$  and  $\forall a \in L_M$ ,  $b \in L$  and  $b \land b \land a \Rightarrow a = b$  then the program is free of deadlocks.

### Freedom of Deadlock

The following holds for a program with mutexes and monitors:

#### Theorem (freedom of deadlock)

If there exists no  $a \in L$  with  $a \prec a$  then the program is free of deadlocks.

The following holds for a program with mutexes and monitors:

#### Theorem (freedom of deadlock)

**Freedom of Deadlock** 

If there exists no  $a \in L$  with  $a \prec a$  then the program is free of deadlocks.

Suppose a program blocks on semaphores (mutexes)  $L_S$  and on monitors  $L_M$  such that  $L=L_S\cup L_M$ .

#### Theorem (freedom of deadlock for monitors)

If  $\forall a \in L_S . a \not\prec a$  and  $\forall a \in L_M, b \in L . a \prec b \land b \prec a \Rightarrow a = b$  then the program is free of deadlocks.

Note: the set L contains *instances* of a lock.

- the set of lock instances can vary at runtime
- if we statically want to ensure that deadlocks cannot occur:
  - summarize every lock/monitor that may have several instances into one
  - $lackbox{ a summary lock/monitor } ar{a} \in L_M ext{ represents several concrete ones}$
  - ▶ thus, if  $\bar{a} \prec \bar{a}$  then this might not be a self-cycle
  - ightarrow require that  $ar{a} 
    ot\prec ar{a}$  for all summarized monitors  $ar{a} \in L_M$

## **Avoiding Deadlocks in Practice**

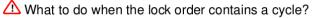
How can we verify that a program contains no deadlocks?

- lacksquare identify mutex locks  $L_S$  and summarized monitor locks  $L_M^s \subseteq L_M$
- oldertify non-summary monitor locks  $L_M^n = L_M \setminus L_M^s$
- sort locks into ascending order according to lock sets
- check that no cycles exist except for self-cycles of non-summary monitors

**Avoiding Deadlocks in Practice** 

How can we verify that a program contains no deadlocks?

- lacktriangledown identify mutex locks  $L_S$  and summarized monitor locks  $L_M^s \subseteq L_M$
- lacksquare identify non-summary monitor locks  $L_M^n = L_M \setminus L_M^s$
- sort locks into ascending order according to lock sets
- check that no cycles exist except for self-cycles of non-summary monitors



- determining which locks may be acquired at each program point is undecidable → lock sets are an approximation
- ullet an array of locks in  $L_S$ : lock in increasing array index sequence
- if  $l \in \lambda(P)$  exists  $l' \prec l$  is to be acquired  $\leadsto$  change program: release l, acquire l', then acquire l again  $\leadsto$  inefficient
- if a lock set contains a summarized lock  $\bar{a}$  and  $\bar{a}$  is to be acquired, we're stuck

Monitors Locks and Monitors

Deadlocks

Deadlock Prevention

Atomic Exec

Deadlock

eadlock Prevention

22 / 20

# **Avoiding Deadlocks in Practice**



How can we verify that a program contains no deadlocks?

- lacksquare identify mutex locks  $L_S$  and summarized monitor locks  $L_M^s \subseteq L_M$
- **3** identify non-summary monitor locks  $L_M^n = L_M \setminus L_M^s$
- sort locks into ascending order according to lock sets
- check that no cycles exist except for self-cycles of non-summary monitors

 $\triangle$  What to do when the lock order contains a cycle?

- determining which locks may be acquired at each program point is undecidable → lock sets are an approximation
- $\bullet$  an array of locks in  $\mathcal{L}_{\mathcal{S}} :$  lock in increasing array index sequence
- if  $l \in \lambda(P)$  exists  $l' \prec l$  is to be acquired  $\leadsto$  change program: release l, acquire l', then acquire l again  $\leadsto$  inefficient
- $\bullet$  if a lock set contains a summarized lock  $\bar{a}$  and  $\bar{a}$  is to be acquired, we're stuck

an example for the latter is the Foo class: two instances of the same class call each other

**Locks Roundup** 

omic Executions, Locks and Monitors

Deadlock

Deadlock Prevention

Atomic Francisco I calco and Maniton

Locks Roundup

#### **Atomic Execution and Locks**



Consider replacing the specific locks with atomic annotations:

```
stack: removal

void pop() {
    ...
    wait(q->t);
    ...
    if (*) { signal(q->t); return; }
    ...
    if (c) wait(q->s);
    ...
    if (c) signal(q->s);
    signal(q->t);
}
```

tomic Executions, Locks and Monitors

Locks Roundur

25 / 2

## **Atomic Execution and Locks**

Consider replacing the specific locks with atomic annotations:

```
stack: removal

void pop() {
    ...
    wait(q->t);
    ...
    if (*) { signal(q->t); return; }
    ...
    if [c] wait(q->s);
    ...
    if (c) signal(q->s);
    signal(q->t);
}
```

nested atomic blocks still describe one atomic execution

→ locks convey additional information over atomic

locks cannot easily be recovered from atomic declarations

Atomic Executions, Locks and Monitors

Locks Roundur

. . . . . .

## **Outlook**



Writing atomic annotations around sequences of statements is a convenient way of programming.

# Outlook



Writing atomic annotations around sequences of statements is a convenient way of programming.

Idea of mutexes: Implement atomic sections with locks:

- a single lock could be used to protect all atomic blocks
- more concurrency is possible by using several locks
- some statements might modify variables that are never read by other threads → no lock required
- statements in one atomic block might access variables in a different order to another atomic block 
   → deadlock possible with locks implementation
- ullet creating too many locks can decrease the performance, especially when required to release locks in  $\lambda(l)$  when acquiring l

Atomic Executions, Locks and Monitors

Laste Davidos

20 / 20

Atomic Executions, Locks and Monitors

Locks Roundu

## **Concurrency across Languages**



In most systems programming languages (C,C++) we have

- the ability to use atomic operations
- we can implement wait-free algorithms

# Concurrency across Languages



In most systems programming languages (C,C++) we have

- the ability to use atomic operations
- we can implement wait-free algorithms

In Java, C# and other higher-level languages

- provide monitors and possibly other concepts
- often simplify the programming but incur the same problems

Atomic Executions, Locks and Monitors

Locks Roundup

07/00

Locks Roundu

\_\_\_\_

# **Concurrency across Languages**



In most systems programming languages (C,C++) we have

- the ability to use atomic operations
- we can implement wait-free algorithms

In Java, C# and other higher-level languages

- provide monitors and possibly other concepts
- often simplify the programming but incur the same problems

language	barriers	wait-/lock-free	semaphore	mutex	m	monitor	
C,C++	<b>_</b>	<b>√</b>	<b>√</b>	<b>√</b>		(a)	
Java,C#	-	(b)	(c)	$\checkmark$		$\checkmark$	

- (a) some pthread implementations allow a reentrant attribute
- (b) newer API extensions ( java.util.concurrent.atomic.\* and System.Threading.Interlocked resp.)
- (c) simulate semaphores using an object with two synchronized methods

# **Summary**



Classification of concurrency algorithms:

- wait-free, lock-free, locked
- next on the agenda: transactional

Wait-free algorithms:

- never block, always succeed, never deadlock, no starvation
- very limited in expressivity

Lock-free algorithms:

- never block, may fail, never deadlock, may starve
- invariant may only span a few bytes (8 on Intel)

Locking algorithms:

- can guard arbitrary code
- can use several locks to enable more fine grained concurrency
- may deadlock
- semaphores are not re-entrant monitors are

use algorithm that is best fit

37 / 39 Atomic Executions

Locks Roundup

# References

E. G. Coffman, M. Elphick, and A. Shoshani. System deadlocks. *ACM Comput. Surv.*, 3(2):67–78, June 1971. ISSN 0360-0300.

T. Harris, J. Larus, and R. Rajwar.

Transactional memory, 2nd edition.

Synthesis Lectures on Computer Architecture, 5(1):1–263, 2010.

Atomic Executions, Locks and Monitor

Locks Poundur

39 / 39		