Script generated by TTT

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Why Memory Barriers are not Enough



Communication via memory barriers has only specific applications:

- coordinating state transitions between threads
- for systems that require minimal overhead (and no de-scheduling)

Often certain pieces of memory may only be modified by one thread at once.

- can use barriers to implement automata that ensure *mutual exclusion*
- --- generalize the re-occurring concept of enforcing mutual exclusion



TECHNISCHE UNIVERSITÄT FAKULTÄT INFORMATIK

19,11 there is a lecture

Programming Languages

Concurrency: Atomic Executions, Locks and Monitors

Dr. Axel Simon and Dr. Michael Petter Winter term 2014

Implementation of a Basic Monitor



A monitor contains a mutex s and the thread currently occupying it:

```
typedef struct monitor mon_t;
struct monitor { int tid; int count; };
void monitor_init(mon_t* m) { memset(m, 0, sizeof(mon_t)); }
```

Define monitor_enter and monitor_leave:

- ensure mutual exclusion of accesses to mon t
- track how many times we called a monitored procedure recursively

```
void monitor_enter(mon_t *m) {
                                    void monitor_leave(mon_t *m) {
  bool mine = false;
                                      atomic {
  while (!mine) {
                                        m->count--:
                                        if (m->count==0) {
    atomic {
      mine = thread_id()==m->tid;
                                          // wake up threads
      if (mine) m->count++; else
                                          m->tid=0;
        if (m->tid==0) {
          mine = true; m->count=1;
          m->tid = thread_id();
        }
    };
    if (!mine) de_schedule(&m->tid);}}
```

Rewriting the Queue using Monitors



Instead of the mutex, we can now use monitors to protect the queue:

```
double-ended queue: monitor version

void PushLeft(DQueue* q, int val) {
    monitor_enter(q->m);
    ...
    monitor_leave(q->m);
}

void ForAll(DQueue* q, void* data, void (*callback)(void*,int)){
    monitor_enter(q->m);
    for (QNode* qn = q->left->right; qn!=q->right; qn=qn->right)
        (*callback)(data, qn->val);
    monitor_leave(q->m);
}
```

Recursive calls possible:

Atomic Executions, Locks and Monitors

Locked Atomic Executions

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Locked Atomic Execution

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Recursive calls possible:

- the function passed to ForAll can invoke PushLeft
- example: ForAll(q,q,&PushLeft) duplicates entries
- using monitor instead of mutex ensures that recursive call does not block

Atomic Executions, Locks and Monitors

Locked Atomic Executions

Atomic Executions, Locks and Monitors

Locked Atomic Executions

Condition Variables



√ Monitors simplify the construction of thread-safe resources. Still: Efficiency problem when using resource to synchronize:

- if a thread t waits for a data structure to be filled:
 - ▶ t will call e.g. PopRight and obtain -1
 - t then has to call again, until an element is available
 - $ightharpoonup \Delta t$ is busy waiting and produces contention on the lock

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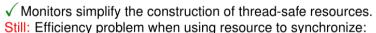
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Idea: create a *condition variable* on which to block while waiting:

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struct monitor { int tid; int count; int cond; };
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Atomic Executions, Locks and Monitors

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struct monitor { int tid; int count; int cond; };

Define these two functions:

- wait for the condition to become true
 - called while being *inside* the monitor
 - ► temporarily *releases* the monitor and blocks
 - when signalled, re-acquires the monitor and returns
- signal waiting threads that they may be able to proceed
 - one/all waiting threads that called wait will be woken up, two possibilities: signal-and-urgent-wait: the signalling thread suspends and continues once the signalled thread has released the monitor

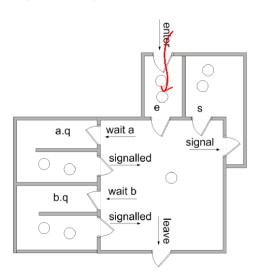
signal-and-continue the signalling thread continues, any signalled thread enters when the monitor becomes available

Signal-And-Urgent-Wait Semantics



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Requires one gueues for each condition c and a suspended gueue s:



a thread who tries to enter a monitor is added to gueue e if the monitor is occupied

SOURCE: http://en.wikipedia.org/wiki/Monitor_(synchronization)

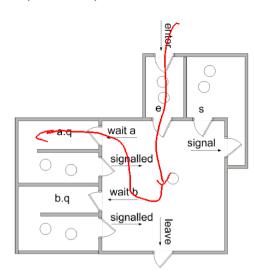
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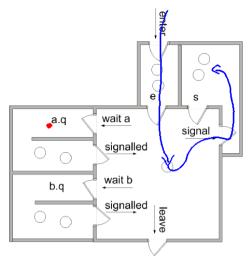
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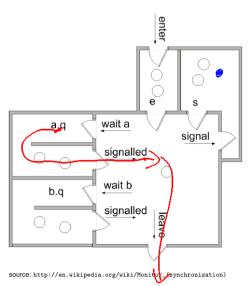
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Atomic Executions, Locks and Monitors

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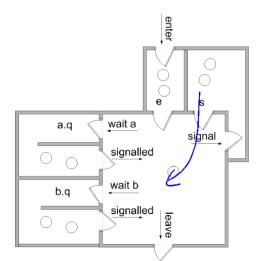


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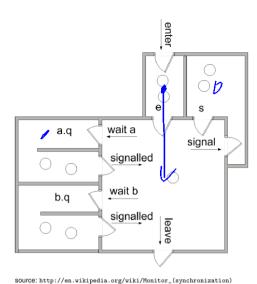
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Locked Atomic Executions

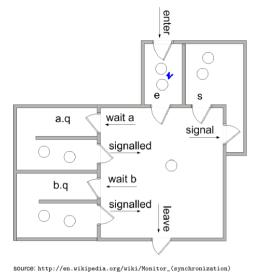
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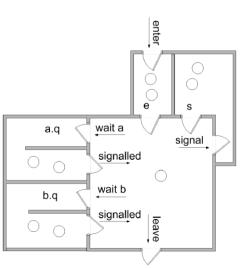
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Signal-And-Urgent-Wait Semantics





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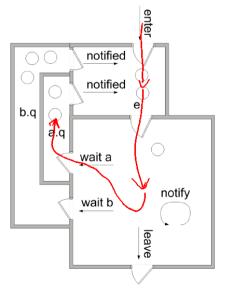
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Signal-And-Continue Semantics



Here, the signal function is usually called notify.



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Locked Atomic Executions

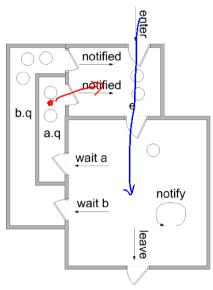
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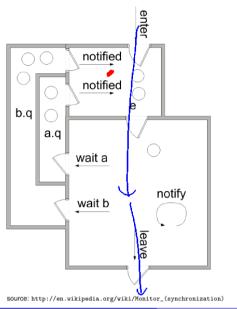
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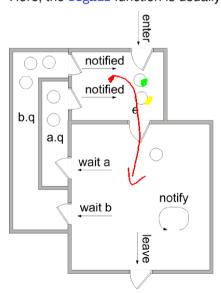


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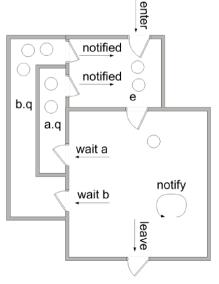
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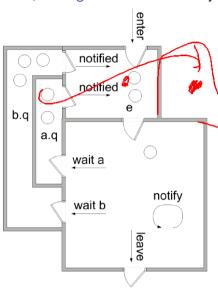
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Atomic Executions, Locks and Monitors

Signal-And-Continue Semantics



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• a call to wait on condition a adds thread to the queue a.q

- a call to notify for a adds one thread from a.q to e (unless a.q is empty)
- ullet if a thread leaves, it wakes up one thread waiting on e
- ightarrow signalled threads compete for the monitor
- assuming FIFO ordering on e, threads who tried to enter between wait and notify will run first
- need additional queue s if waiting threads should have priority

Atomic Executions, Locks and Monitors

Locked Atomic Executions

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A Note on Notify



With *signal-and-continue* semantics, two notify functions exist:

- notify: wakes up exactly one thread waiting on condition variable
- notifyAll: wakes up all threads waiting on a condition variable

Implementing Condition Variables



We implement the simpler *signal-and-continue* semantics:

a notified thread is simply woken up and competes for the monitor

```
void cond_wait(mon_t *m) {
  assert(m->tid==thread_id());
  int old_count = m->count;
  m->tid = 0;
  wait(m->cond);
  bool next_to_enter;
  do {
                                       void cond_notify(mon_t *m) {
    atomic {
                                          // wake up other threads
                                          signal(m->cond);
      next_to_enter = m->tid==0;
      if (next to enter) {
        m->tid = thread_id();
        m->count = old_count;
    if (!next_to_enter) de_schedule(&m->tid);
  } while (!next_to_enter);}
```

Atomic Executions, Locks and Monitors

ocked Atomic Executions

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an implementation often becomes easier if notify means notify some

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 - a notified thread is likely to block immediately on &m->tid

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- a notified thread is likely to block immediately on &m->tid
- motified threads compete for the monitor with other threads
- if OS implements FIFO order: notified threads will run after threads that tried to enter since wait was called

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Implementing PopRight with Monitors



We use the monitor q->m and the condition variable q->c. PopRight:

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double-ended queue: removal
  int PopRight(DQueue* q, int val) {
    QNode* oldRightNode;
    monitor_enter(q->m); // wait to enter the cr
L: QNode* rightSentinel = q->right;
    oldRightNode = rightSentinel->left;
    if (oldRightNode==leftSentinel) { cond_wait(q->c); goto L; }
    QNode* newRightNode = oldRightNode->left;
    newRightNode->right = rightSentinel;
    rightSentingel->left = newRightNode;
    monitor_leave(q->m); // signal that we're done
    int val = oldRightNode->val;
    free(oldRightNode);
    return val;
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}
```

- if the queue is empty, wait on q->c
- use a loop, in case the thread is woken up spuriously

Atomic Executions, Locks and Monitors

Locked Atomic Executions

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Monitor versus Semaphores



A monitor can be implemented using semaphores:

protect each queue with a mutex

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Monitor versus Semaphores



A monitor can be implemented using semaphores:

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A semaphore can be implemented using a monitor:

protect the semaphore variable s with a monitor

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Atomic Executions, Locks and Monitors

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- \(\text{difficult to implement general conditions} \)
 - OS would have to run code to determine if p holds
 - OS would have to ensure atomicity
 - problematic if p is implemented by arbitrary code

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Atomic Executions, Locks and Monitors

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Atomic Executions, Locks and Monitors

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 - notify variable if the predicate may have changed



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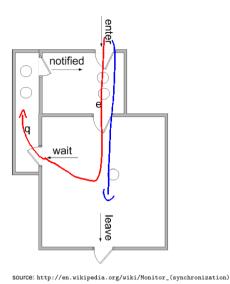
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 - wake up thread and have it check the predicate itself
- create condition variable for each set of threads with the same p
 - notify variable if the predicate may have changed
- or, simpler: notify all threads each time any predicate changes
 - without predicates, a single condition variable suffices!

Atomic Executions, Locks and Monitors

Monitors with a Single Condition Variable



Monitors with a single condition variable are built into Java and C#:



```
class C {
  public synchronized void f() {
   // body of
is equivalent to
class C {
```

public void f() { monitor_enter(); // body of f monitor_leave();

with Object containing: private int mon_var;

private int mon_count; private int cond_var; protected void monitor_enter();

protected void monitor_leave();

Atomic Executions, Locks and Monitors

Deadlocks with Monitors



Definition (Deadlock)

A deadlock is a situation in which two processes are waiting for the respective other to finish, and thus neither ever does.

(The definition generalizes to a set of actions with a cyclic dependency.)

Atomic Executions, Locks and Monitors

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(The definition generalizes to a set of actions with a cyclic dependency.)

Consider this Java class:

Sequence leading to a deadlock:

```
class Foo {
  public Foo other = null;
  public synchronized void bar() {
    ... if (*) other.bar(); ...
and two instances:
  Foo a = new Foo();
  Foo b = new Foo();
 a.other = b; b.other = a;
  // in parallel:
 a.bar() || b.bar();
```

Deadlocks with Monitors



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• threads A and B execute a.bar() and b.bar() public synchronized void bar() {

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Deadlocks with Monitors



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Atomic Executions, Locks and Monitors

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Sequence leading to a deadlock:

 A happens to execute other.bar()

and b.bar()

• threads A and B execute a.bar()

• b.bar() acquires the monitor of b

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- threads A and B execute a.bar()
- and b.bar()
- b.bar() acquires the monitor of b
- A happens to execute other.bar()
- A blocks on the monitor of b
- B happens to execute other.bar()

Treatment of Deadlocks



Deadlocks occur if the following four conditions hold [Coffman et al.(1971)Coffman, Elphick, and Shoshani]:

- mutual exclusion: processes require exclusive access
- 2 wait for: a process holds resources while waiting for more
- no preemption: resources cannot be taken away form processes
- circular wait: waiting processes form a cycle

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Atomic Executions, Locks and Monitors



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- prevention: design programs to be deadlock-free
- avoidance: use additional information about a program that allows the OS to schedule threads so that they do not deadlock
- → prevention is the only safe approach on standard operating systems
 - can be achieve using lock-free algorithms
 - but what about algorithms that require locking?

Atomic Executions, Locks and Monitors Atomic Executions, Locks and Monitors

Deadlock Prevention through Partial Order



Observation: A cycle cannot occur if locks can be partially ordered.

Definition (lock sets)

Let L denote the set of locks. We call $\lambda(p) \subseteq L$ the lock set at p, that is, the set of locks that may be in the "acquired" state at program point p.

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Let $\sigma \subseteq X \times X$ be a relation. Its transitive closure is $\sigma^+ = \bigcup_{i \in \mathbb{N}} \sigma^i$ where

$$\frac{\sigma^0}{\sigma^{i+1}} = \{\langle x_1, x_3 \rangle \mid \exists x_2 \in X . \langle x_1, x_2 \rangle \in \sigma^i \land \langle x_2, x_3 \rangle \in \sigma^i \}$$

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Each time a lock is acquired, we track the lock set at p:

Definition (lock order)

Define $\triangleleft \subseteq L \times L$ such that $l \triangleleft l'$ iff $l \in \lambda(p)$ and the statement at p is of the form wait(1') or monitor_enter(1'). Define the strict lock order $\prec = \lhd^+$.

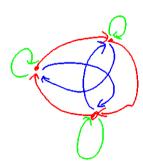
Freedom of Deadlock



The following holds for a program with mutexes and monitors:

Theorem (freedom of deadlock)

If there exists no $a \in L$ with $a \prec a$ then the program is free of deadlocks.



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Suppose a program blocks on semaphores (mutexes) L_S and on monitors L_M such that $L=L_S \cup L_M$.

Theorem (freedom of deadlock for monitors)

If $\forall a \in L_S . a \not\prec a$ and $\forall a \in L_M, b \in L . a \prec b \land b \prec a \Rightarrow a = b$ then the program is free of deadlocks.

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Locked Atomic Executions

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Avoiding Deadlocks in Practice



How can we verify that program contains no deadlocks?

ullet identify mutex locks L_S and summarized monitor locks $L_M^s\subseteq L_M$

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Note: the set L contains *instances* of a lock.

- the set of lock instances can vary at runtime
- if we statically want to ensure that deadlocks cannot occur:
 - summarize every lock/monitor that may have several instances into one
 - lackrow a summary lock/moitor $\bar{a} \in L_M$ represents several concrete ones
 - ▶ thus, if $\bar{a} \prec \bar{a}$ then this might not be a self-cycle
 - ightharpoonup require that $\bar{a} \not\prec \bar{a}$ for all summarized monitors $\bar{a} \in L_M$

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Avoiding Deadlocks in Practice



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⚠ What to do when lock order contains cycle?



- determining which locks may be acquired at each program point is undecidable --- lock sets are an approximation
- ullet an array of locks in L_S : lock in increasing array index sequence
- if $l \in \lambda(P)$ exists $l' \prec l$ is to be acquired \leadsto change program: release l, acquire l', then acquire l again \leadsto inefficient
- if a lock set contains a summarized lock \bar{a} and \bar{a} is to be acquired, we're stuck

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an example for the latter is the Foo class: two instances of the same class call each other

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Refining the Queue: Concurrent Access



Add a second lock s->t to allow concurrent removal:

```
double-ended queue: removal
  int PopRight(DQueue* q) {
    QNode* oldRightNode;
    wait(q->t); // wait to enter the critic
    QNode* rightSentinel = q->right;
   oldRightNode = rightSentinel->left;
    if (oldRightNode==leftSentinel) { signal(q->t); return -1; }
    QNode* newRightNode = oldRightNode->left;
    int c = newRightNode==leftSentinel;
    if (c) wait(q->s);
   newRightNode->right = rightSentinel; / LS
   rightSentinel->left = newRightNode;
   if (c) signal(q->s);
   signal(q->t); // signal that we're done
   int val = oldRightNode->val;
    free(oldRightNode);
    return val:
```

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Example: Deadlock freedom



Is the example deadlock free? Consider its skeleton:

```
double-ended queue: removal

void PopRight() {
    ...
    wait(q->t);
    ...
    if (*) { signal(q->t); return; }
    ...
    if (c) wait(q->s);
    ...
    if (c) signal(q->s);
    signal(q->t);
}
```

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Example: Deadlock freedom



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void PopRight() {
    ...
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    ...
    if (*) { signal(q->t); return; }

p: ...
    if (c) wait(q->s);
        ...
    if (c) signal(q->s);
        signal(q->t);
}
```

- in PushLeft, the lock set for s is empty
- ullet here, the lock set of s is $\{t\}$
- $\bullet \ t \lhd s$ and transitive closure is $t \prec s$



• which the program cannot deadlock

Atomic Execution and Locks



Consider replacing the specific locks with atomic annotations:

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Atomic Execution and Locks



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```

- nested atomic blocks still describe one atomic execution
- --- locks convey additional information over atomic
- locks cannot easily be recovered from atomic declarations

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Outlook



Writing atomic annotations around sequences of statements is a convenient way of programming.

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Outlook



Writing atomic annotations around sequences of statements is a convenient way of programming.

Idea of mutexes: Implement atomic sections with locks:

- a single lock could be use to protect all atomic blocks
- more concurrency is possible by using several locks
 see the PushLeft, PopRight example
- some statements might modify variables that are never read by other threads → no lock required
- statements in one atomic block might access variables in a different order to another atomic block → deadlock possible with locks implementation
- ullet creating too many locks can decrease the performance, especially when required to release locks in $\lambda(l)$ when acquiring l

Concurrency across Languages



In most systems programming languages (C,C++) we have

the ability to use atomic operations

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In Java, C# and other higher-level languages

- provide monitors and possibly other concepts
- often simplify the programming but incur the same problems

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Concurrency across Languages



In most systems programming languages (C,C++) we have

- the ability to use *atomic* operations
- ullet we can implement wait-free algorithms

In Java, C# and other higher-level languages

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language	barriers	wait-/lock-free	semaphore	mutex	monitor
C,C++	V/	V -	1	V/	(a) /
Java,C#	-	-	(b)	1	V /

- (a) some pthread implementations allow a reentrant attribute
- (b) simulate semaphores using an object with two synchronized methods

Summary



Classification of concurrency algorithms:

- wait-free, lock-free, locked
- next on the agenda: transactional

Wait-free algorithms:

- never block, always succeed, never deadlock, no starvation
- very limited in what they can do

Lock-free algorithms:

- never block, may fail, never deadlock, may starve
- invariant may only span a few bytes (8 on Intel)

Locking algorithms:

- can guard arbitrary code versonces
- can use several locks to enable more fine grained concurrency
- may deadlock
- semaphores are not re-entrant, monitors are
- → use algorithm that is best fit

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