

Title: Seidl: Virtual_Machines (23.04.2013)

Date: Tue Apr 23 14:01:31 CEST 2013

Duration: 91:29 min

Pages: 29

Simplification:

We only regard switch-statements of the following form:

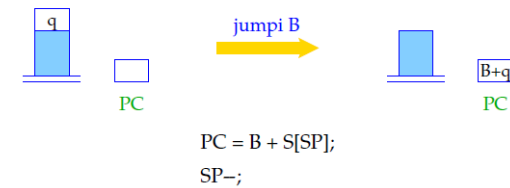
```
s ≡ switch (e) {  
  case 0: ss0 break;  
  case 1: ss1 break;  
  ⋮  
  case k - 1: ssk-1 break;  
  default: ssk  
}
```

s is then translated into the instruction sequence:

4.5 The switch-Statement

Idea:

- Multi-target branching in constant time!
- Use a jump table, which contains at its *i*-th position the jump to the beginning of the *i*-th alternative.
- Realized by indexed jumps.



```
code s ρ = codeR e ρ      C0: code ss0 ρ      B: jump C0  
           check 0 k B      jump D          ⋮  
           ⋮                ⋮                jump Ck  
           Ck: code ssk ρ      D: ⋮  
           jump D
```

- The Macro `check 0 k B` checks, whether the R-value of *e* is in the interval $[0, k]$, and executes an indexed jump into the table `B`
- The jump table contains direct jumps to the respective alternatives.
- At the end of each alternative is an unconditional jump out of the switch-statement.

```

code s ρ = codeR e ρ      C0: code s0 ρ      B: jump C0
           check 0 k B      jump D          ...
           ...              ...              jump Ck
           Ck: code sk ρ      D: ...
           jump D

```

- The Macro `check 0 k B` checks, whether the R-value of e is in the interval $[0, k]$, and executes an indexed jump into the table `B`
- The jump table contains direct jumps to the respective alternatives.
- At the end of each alternative is an unconditional jump out of the switch-statement.

41

```

check 0 k B = dup          dup          jumpi B
              loadc 0      loadc k      A: pop
              geq          le           loadc k
              jumpz A      jumpz A      jumpi B

```

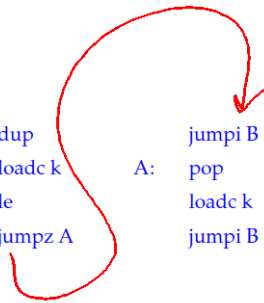
- The R-value of e is still needed for indexing after the comparison. It is therefore copied before the comparison.
- This is done by the instruction `dup`.
- The R-value of e is replaced by k before the indexed jump is executed if it is less than 0 or greater than k .

42

```

check 0 k B = dup          dup          jumpi B
              loadc 0      loadc k      A: pop
              geq          le           loadc k
              jumpz A      jumpz A      jumpi B

```



- The R-value of e is still needed for indexing after the comparison. It is therefore copied before the comparison.
- This is done by the instruction `dup`.
- The R-value of e is replaced by k before the indexed jump is executed if it is less than 0 or greater than k .

42

Note:

- The jump table could be placed directly after the code for the Macro `check`. This would save a few unconditional jumps. However, it may require to search the switch-statement twice.
- If the table starts with u instead of 0, we have to decrease the R-value of e by u before using it as an index.
- If all potential values of e are **definitely** in the interval $[0, k]$, the macro `check` is not needed.

44

5 Storage Allocation for Variables

Goal:

Associate **statically**, i.e. at compile time, with each variable x a fixed (relative) address ρx

Assumptions:

- variables of basic types, e.g. `int`, ... occupy one storage cell.
- variables are allocated in the store in the order, in which they are declared, starting at address 1.

Consequently, we obtain for the declaration $d \equiv t_1 x_1; \dots t_k x_k;$ (t_i basic type) the address environment ρ such that

$$\rho x_i = i, \quad i = 1, \dots, k$$

45

5 Storage Allocation for Variables

Goal:

Associate **statically**, i.e. at compile time, with each variable x a fixed (relative) address ρx

Assumptions:

- variables of basic types, e.g. `int`, ... occupy one storage cell.
- variables are allocated in the store in the order, in which they are declared, starting at address 1.

Consequently, we obtain for the declaration $d \equiv t_1 x_1; \dots t_k x_k;$ (t_i basic type) the address environment ρ such that

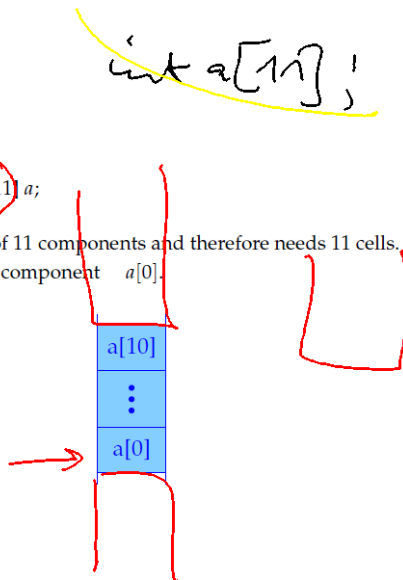
$$\rho x_i = i, \quad i = 1, \dots, k$$

45

5.1 Arrays

Example: `int [11] a;`

The array a consists of 11 components and therefore needs 11 cells. ρa is the address of the component $a[0]$.



46

We need a function `sizeof` (notation: $|\cdot|$), computing the space requirement of a type:

$$|t| = \begin{cases} 1 & \text{if } t \text{ basic} \\ k \cdot |t'| & \text{if } t \equiv t'[k] \end{cases}$$

Accordingly, we obtain for the declaration $d \equiv t_1 x_1; \dots t_k x_k;$

$$\begin{aligned} \rho x_1 &= 1 \\ \rho x_i &= \rho x_{i-1} + |t_{i-1}| \quad \text{for } i > 1 \end{aligned}$$

Since $|\cdot|$ can be computed at compile time, also ρ can be computed at compile time.

47

Task:

Extend `codeL` and `codeR` to expressions with accesses to array components.

Be `t[c] a;` the declaration of an array `a`.

To determine the start address of a component `a[i]`, we compute $\rho a + |t| * (R\text{-value of } i)$.

In consequence:

```
codeL a[e] ρ = loadc (ρ a)
               codeR e ρ
               loadc |t|
               mul
               add
```

... or more general:

48

a

```
codeL e1[e2] ρ = codeR e1 ρ = code a ρ
                  codeR e2 ρ
                  loadc |t|
                  mul
                  add
```

Remark:

- In **C**, an array is a **pointer**. A declared array `a` is a **pointer-constant**, whose R-value is the start address of the array.
- Formally, we define for an array `e`: $\text{code}_R e \rho = \text{code}_L e \rho$
- In **C**, the following are equivalent (as L-values):

`2[a]` `a[2]` `a + 2`

Normalization: Array names and expressions evaluating to arrays occur in front of index brackets, index expressions inside the index brackets.

49

Task:

Extend `codeL` and `codeR` to expressions with accesses to array components.

Be `t[c] a;` the declaration of an array `a`.

To determine the start address of a component `a[i]`, we compute $\rho a + |t| * (R\text{-value of } i)$.

In consequence:

```
codeL a[e] ρ = loadc (ρ a)
               codeR e ρ
               loadc |t|
               mul
               add
```

... or more general:

48

5.2 Structures

In **Modula** and **Pascal**, structures are called **Records**.

Simplification:

Names of structure components are not used elsewhere. Alternatively, one could manage a separate environment ρ_{st} for each structure type `st`.

Be `struct { int a; int b; } x;` part of a declaration list.

- `x` has as relative address the address of the first cell allocated for the structure.
- The components have addresses **relative** to the start address of the structure. In the example, these are $a \mapsto 0, b \mapsto 1$.

50

Let $t \equiv \text{struct } \{t_1 c_1; \dots t_k c_k\}$. We have

$$|t| = \sum_{i=1}^k |t_i|$$

$$\rho c_1 = 0 \text{ and}$$

$$\rho c_i = \rho c_{i-1} + |t_{i-1}| \text{ for } i > 1$$

We thus obtain:

$$\text{code}_L(e.c) \rho = \text{code}_L e \rho$$

$$\text{loadc}(\rho c)$$

$$\text{add}$$

51

5.2 Structures

In **Modula** and **Pascal**, structures are called **Records**.

Simplification:

Names of structure components are not used elsewhere. Alternatively, one could manage a separate environment ρ_{st} for each structure type st .

Be $\text{struct } \{ \text{int } a; \text{int } b; \} x;$ part of a declaration list.

- x has as relative address the address of the first cell allocated for the structure.
- The components have addresses **relative** to the start address of the structure. In the example, these are $a \mapsto 0, b \mapsto 1$.

50

Let $t \equiv \text{struct } \{t_1 c_1; \dots t_k c_k\}$. We have

$$|t| = \sum_{i=1}^k |t_i|$$

$$\rho c_1 = 0 \text{ and}$$

$$\rho c_i = \rho c_{i-1} + |t_{i-1}| \text{ for } i > 1$$

We thus obtain:

$$\text{code}_L(e.c) \rho = \text{code}_L e \rho$$

$$\text{loadc}(\rho c)$$

$$\text{add}$$

51

Example:

Be $\text{struct } \{ \text{int } a; \text{int } b; \} x;$ such that $\rho = \{x \mapsto 13, a \mapsto 0, b \mapsto 1\}$.

This yields:

$$\text{code}_L(x.b) \rho = \text{loadc } 13$$

$$\text{loadc } 1$$

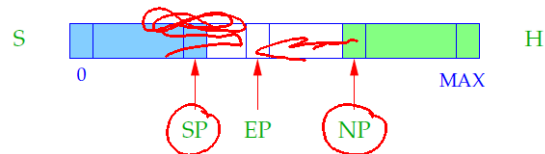
$$\text{add}$$

52

6 Pointer and Dynamic Storage Management

Pointer allow the access to anonymous, dynamically generated objects, whose life time is not subject to the LIFO-principle.

⇒ We need another potentially unbounded storage area H – the Heap.



NP $\hat{=}$ New Pointer; points to the lowest occupied heap cell.

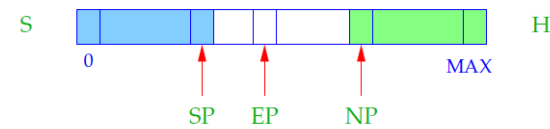
EP $\hat{=}$ Extreme Pointer; points to the uppermost cell, to which SP can point (during execution of the actual function).

53

6 Pointer and Dynamic Storage Management

Pointer allow the access to anonymous, dynamically generated objects, whose life time is not subject to the LIFO-principle.

⇒ We need another potentially unbounded storage area H – the Heap.



NP $\hat{=}$ New Pointer; points to the lowest occupied heap cell.

EP $\hat{=}$ Extreme Pointer; points to the uppermost cell, to which SP can point (during execution of the actual function).

53

Idea:

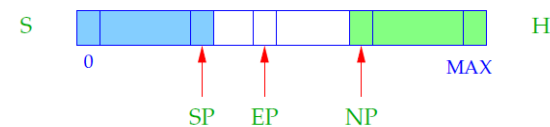
- Stack and Heap grow toward each other in S, but must not collide. (Stack Overflow).
- A collision may be caused by an increment of SP or a decrement of NP.
- EP saves us the check for collision at the stack operations.
- The checks at heap allocations are still necessary.

54

6 Pointer and Dynamic Storage Management

Pointer allow the access to anonymous, dynamically generated objects, whose life time is not subject to the LIFO-principle.

⇒ We need another potentially unbounded storage area H – the Heap.



NP $\hat{=}$ New Pointer; points to the lowest occupied heap cell.

EP $\hat{=}$ Extreme Pointer; points to the uppermost cell, to which SP can point (during execution of the actual function).

53

Idea:

- Stack and Heap grow toward each other in S, but must not collide. (Stack Overflow).
- A collision may be caused by an increment of SP or a decrement of NP.
- EP saves us the check for collision at the stack operations.
- The checks at heap allocations are still necessary.

54

What can we do with pointers (pointer values)?

- set a pointer to a storage cell,
- dereference a pointer, access the value in a storage cell pointed to by a pointer.

There are two ways to set a pointer:

- A call `malloc(e)` reserves a heap area of the size of the value of `e` and returns a pointer to this area:

$$\text{code}_R \text{ malloc}(e) \rho = \text{code}_R e \rho \text{ new}$$

- The application of the address operator `&` to a variable returns a pointer to this variable, i.e. its address ($\hat{=}$ L-value). Therefore:

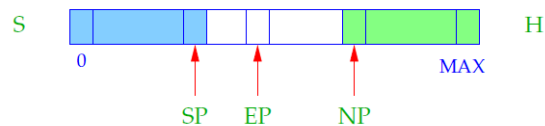
$$\text{code}_R (\&e) \rho = \text{code}_L e \rho$$

55

6 Pointer and Dynamic Storage Management

Pointers allow the access to anonymous, dynamically generated objects, whose life time is not subject to the LIFO-principle.

⇒ We need another potentially unbounded storage area H – the Heap.



- NP $\hat{=}$ New Pointer; points to the lowest occupied heap cell.
- EP $\hat{=}$ Extreme Pointer; points to the uppermost cell, to which SP can point (during execution of the actual function).

53



```

if (NP - S[SP] ≤ EP)
  S[SP] = NULL;
else {
  NP = NP - S[SP];
  S[SP] = NP;
}

```

- NULL is a special pointer constant, identified with the integer constant 0.
- In the case of a collision of stack and heap the NULL-pointer is returned.

56

What can we do with pointers (pointer values)?

- **set** a pointer to a storage cell,
- **dereference** a pointer, access the value in a storage cell pointed to by a pointer.

There are two ways to set a pointer:

- (1) A call `malloc(e)` reserves a heap area of the size of the value of e and returns a pointer to this area:

$$\text{code}_R \text{ malloc}(e) \rho = \text{code}_R e \rho \text{ new}$$

- (2) The application of the address operator `&` to a variable returns a **pointer** to this variable, i.e. its address ($\hat{=}$ **L-value**). Therefore:

$$\text{code}_R (\&e) \rho = \text{code}_L e \rho$$


```

if (NP - S[SP] <= EP)
    S[SP] = NULL;
else {
    NP = NP - S[SP];
    S[SP] = NP;
}
    
```

- NULL is a special pointer constant, identified with the integer constant 0.
- In the case of a collision of stack and heap the NULL-pointer is returned.