

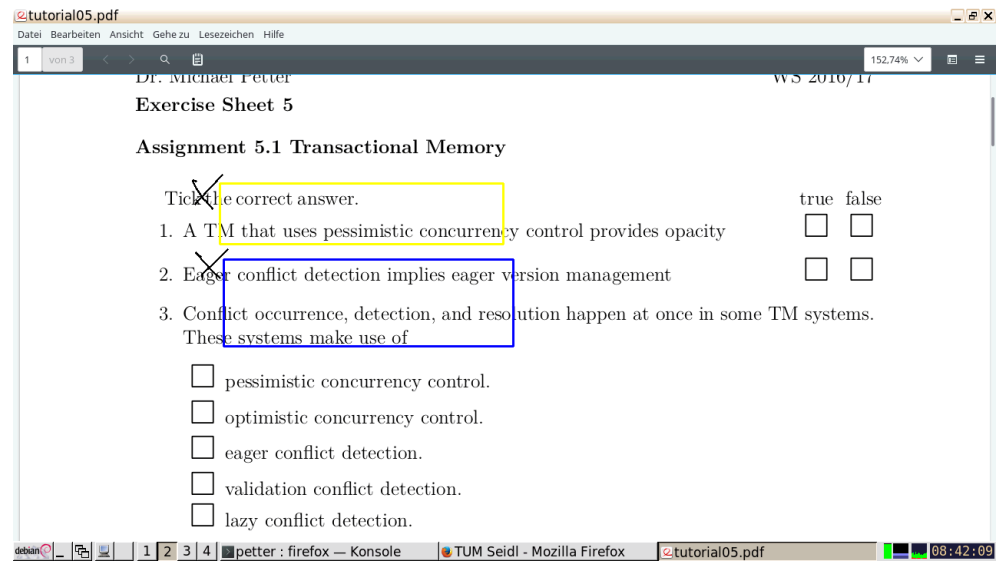
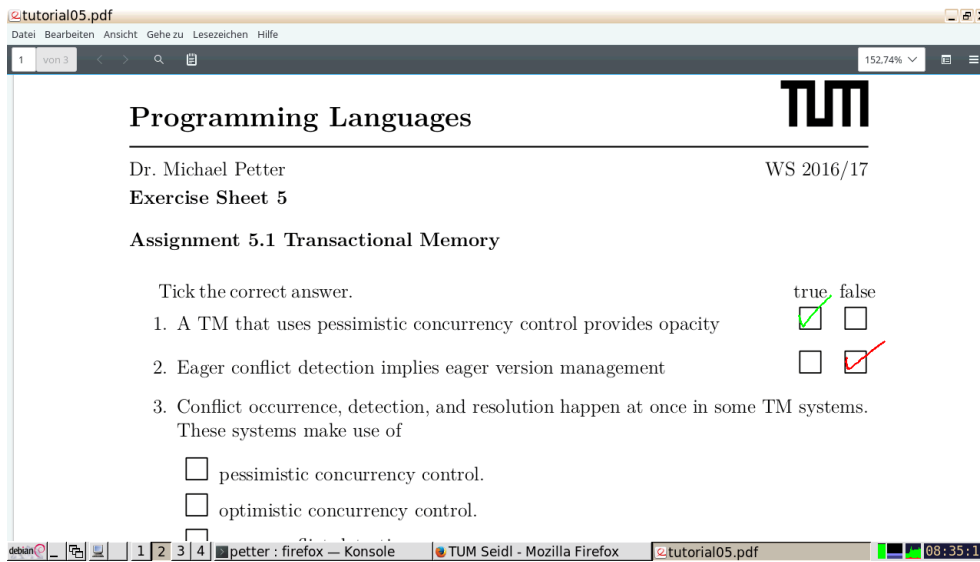
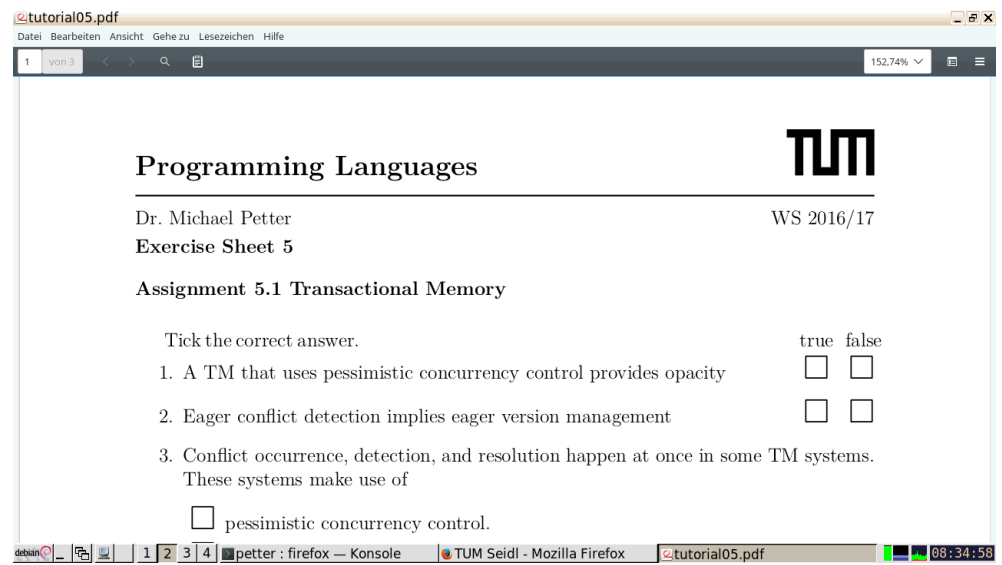
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- pessimistic concurrency control.
- optimistic concurrency control.
- eager conflict detection.
- validation conflict detection.
- lazy conflict detection.

4. A conflict occurs between two threads A and B where B accesses memory in a non-transactional way. The transaction of A consists of two statements s_1, s_2 . B has observed the state after A has executed s_1 but before A has executed s_2 .

- A and B will abort.
- The TM system uses lazy version management.
- The TM does not provide single-lock atomicity.
- The TM does not provide transactional sequential consistency.

5. A zombie transaction crashes the whole program due to reading unexpected and

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- The TM does not provide transactional sequential consistency.

5. A zombie transaction crashes the whole program due to reading unexpected and inconsistent values. The TM implementation

- provides opacity.
- uses pessimistic concurrency control.
- uses eager version management.

6. A TM implementation detects a conflict in the middle of a transaction although the actual write that created the conflict lies in the past. This kind of conflict detection is called

- eager conflict detection.
- tentative conflict detection.
- validation conflict detection.
- lazy conflict detection.

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- uses eager version management.

6. A TM implementation detects a conflict in the middle of a transaction although the actual write that created the conflict lies in the past. This kind of conflict detection is called

- eager conflict detection.
- tentative conflict detection.
- validation conflict detection.
- lazy conflict detection.

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is called

- eager conflict detection.
- tentative conflict detection.
- validation conflict detection.
- lazy conflict detection.

7. A TM implementation uses optimistic concurrency control. Which of the following statements must be false?

- Because a transaction is a zombie, the program crashes after a write through a pointer that is NULL.

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7. A TM implementation uses optimistic concurrency control. Which of the following statements must be false?

- Because a transaction is a zombie, the program crashes after a write through a pointer that is NULL.
- The implementation checks for conflicts only when committing.
- A transaction can be suspended until a conflicting transaction has finished.

Assignment 5.2 Restricted Transactional Memory

Consider the following code fragment on a machine with RTM and Caches:

```
int data = 0;
int s=0;
```

thread P_1 :

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Consider the following code fragment on a machine with RTM and Caches:

```
int data = 0;
int s=0;
```

thread P_1 :

```
if (_xbegin() == -1) {
    data++;
    _xend();
} else {
    data++;
}
```

thread P_0 :

```
while (s != -1)
    if ((s = _xbegin()) == -1) {
        data++;
        _xend();
    }
```

1. Fill in the gap with either "will", "will not" or "may or may not":

After P_0 and P_1 both terminate, data _____ evaluate to 1.

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```
thread  $P_0$ :
```

```
while (s != -1)
    if ((s = _xbegin()) == -1) {
        data++;
        _xend();
    }
```

1. Fill in the gap with either "will", "will not" or "may or may not":

After P_0 and P_1 both terminate, data may or may not evaluate to 1.

2. Fill in the gap with either "will", "will not" or "may or may not":

After P_0 and P_1 both terminate, data will not evaluate to 3.

3. Consider the following interleaving of paths through the program:

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```
if ((s = _xbegin()) == -1) {
    data++;
    _xend();
} else {
    data++;
}
```

1. Fill in the gap with either "will", "will not" or "may or may not":

After P_0 and P_1 both terminate, data _____ evaluate to 1.

2. Fill in the gap with either "will", "will not" or "may or may not":

After P_0 and P_1 both terminate, data _____ evaluate to 3.

3. Consider the following interleaving of paths through the program:

```
P0 s!=-1 (s=_xbegin())=-1 data++;
P1 s=0 (s=_xbegin())=-1 data++; _xend();
```

Draw a happened-before diagram for this interleaving. The initial cache states for P_0 and P_1 are S_0, S_0 .

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```

P0 s!=-1 (s=_xbegin())==-1 data++; s!=-1 (s=_xbegin())==-1 data++ _xend()
P1 _xbegin()==-1 data++;

```

→ time

Draw a happened-before diagram of this interleaving. The initial cache states for **s** and **data** are S_0, S_0 .

P_0 _____

P_1 _____

2

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Draw a happened-before diagram of this interleaving. The initial cache states for **s** and **data** are S_0, S_0 .

P_0 _____

while (s!=-1)
s! =

P_1 _____

2 } *M*

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Consider the following code fragment on a machine with L1M and S0C0:

```

int data = 0;
int s=0;

thread P0:
while (s!=-1)
if((s=_xbegin())==-1){
data++;
_xend();
}

thread P1:
if (_xbegin()==-1){
data++;
_xend();
}else {
data++;
}

```

1. Fill in the gap with either “will”, “will not” or “may or may not”:
 After P_0 and P_1 both terminate, **data** _____ evaluate to 1.

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